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Date: 2/27/2002 Bennett H. Johnson

1 AN OPTICAL SWITCH ARRAY ASSEMBLY FOR DNA PROBE

2 SYNTHESIS AND DETECTION

3 FIELD OF THE INVENTION

4 This invention relates generally to an optical switch array assembly for
5 DNA probe light synthesis and hybridized DNA probe light detection, and
6 particularly to a micromachined optical switches array assembly that can be
7 selectively actuated for DNA probe light synthesis and hybridized DNA probe
8 light detection.

9

10 BACKGROUND OF THE INVENTION

11 With the advance of the human genome program, there is a strong
12 movement to diagnose diseases and understand life phenomena by
13 understanding living bodies on the basis of DNA. The main objective of DNA
14 diagnosis is the development of a simple, accurate and cheap technique for
15 DNA screening. The newly developed DNA chips represent a powerful
16 technique for DNA screening. DNA chips have small size, allow a large
17 reduction of sample and reagent consumption, are quick and can be used
18 simply by untrained operators.

19 DNA arrays have been synthesized using light-directed methods. As an
20 example, light-directed synthesis of oligonucleotides employs 5'-protected
21 nucleosidephosphoramidite "building blocks." The 5'-protecting groups may be
22 either photolabile or acid-labile. A plurality of DNA sequences in predefined
23 regions are synthesized by repeated cycles of deprotection and coupling.

24 Coupling occurs only at sites that have been deprotected. Three methods of
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1 light-directed synthesis are: use of photolabile protecting groups and direct
2 photodeprotection; use of acid-labile 4,4'-dimethoxytrityl (DMT) protecting
3 groups and a photoresist; use of DMT protecting groups and a polymer film that
4 contains a photoacid generator (PAG). These methods have many process
5 steps similar to those used in semiconductor integrated circuit manufacturing.
6 These methods also often involve the use of masks that have a predefined
7 image pattern that permits the light used for synthesis of the DNA arrays to
8 reach certain regions of a substrate but not others. The pattern formed on the
9 mask is projected onto a substrate to define which portions of the wafer are to
10 be deprotected and which regions remain protected. In many cases a different
11 mask having a particular predetermined image pattern is used for each
12 separate masking step, and synthesis of a substrate containing many chips
13 requires a plurality of masking steps with different image patterns. For example,
14 synthesis of an array of 20mers typically requires approximately seventy
15 photolithographic steps and related unique masks. So, using present
16 photolithographic systems and methods, a plurality of different image pattern
17 masks must be pre-generated and changed in the photolithographic system at
18 each masking step. A direct write optical lithography system has been
19 developed to improve the cost, quality, and efficiency of DNA array synthesis
20 by providing a maskless optical lithography system and method where
21 predetermined image patterns can be dynamically changed during
22 photolithographic processing. As such, an optical lithography system is
23 provided to include a means for dynamically changing an intended image
24 pattern without using a mask. One such means includes a spatial light

modulator that is electronically controlled by a computer to generate unique predetermined image patterns at each photolithographic step in DNA array synthesis. The spatial light modulators can be, for example, micromachined mechanical modulators or microelectronic devices. One type of mechanical modulator is a micro-mirror array that uses small metal mirrors to selectively reflect a light beam to particular individual features; thus causing the individual features to selectively receive light from a light source (i.e., turning light on and off of the individual features).

Another type of mechanical modulator is designed to modulate transmitted rather than reflected light. An example of a transmission spatial light modulator is a liquid crystal display (LCD). There are a number of drawbacks with this direct write optical lithography system. First, the system consists of several optical active mechanical alignment apparatus including a mechanism for aligning and focusing the chip or substrate, such as an x-y translation stage and a stepping-motor-driven translation stage for moving the substrate by a distance equal to the desired center-to-center distance between chips. The mass production of the DNA probe arrays spends much labor and time and therefore they are very expensive. Particularly when the density of the cells where the probes are fixed, respectively, in a probe array is large, it is getting difficult to produce the probe array at a low cost.

Second, a complicated apparatus is required for optical detection of a hybridized DNA probe array. This apparatus may involve among others moving a sample substrate while simultaneously detecting light transmitted from one or more sample sites on the substrate by sequentially tracking the sample sites as

1 they move. A stage, movable in a first direction, supports the substrate. A
2 detector detects light emanating from an examination region delimited by a
3 detection initiation position and a detection termination position. An optical relay
4 structure transmits light from the examination region to the detector. A scanning
5 mechanism simultaneously moves the optical relay structure and the substrate
6 in the first direction. The optical relay structure tracks the substrate between the
7 detection initiation position and the detection termination position.

8

9 SUMMARY OF THE INVENTION

10 The present invention is made for removing the above disadvantages,
11 and an object of the present invention is to provide an optical switch array
12 assembly for DNA probe light synthesis and hybridized DNA probe light
13 detection without any moving apparatus for light alignment and probes tracking.

14 Another object of the present invention is to provide an optical switch
15 array assembly that is integrated in a substrate with an optical switch array and
16 at least a driving circuit so that not only each site but also each group of sites
17 for DNA probe synthesis can be selectively illuminated.

18 Still another object of the present invention is to provide an optical switch
19 array that is integrated in a substrate with an optical switch array and at least a
20 driving circuit so that not only each hybridized DNA probe but also each group
21 of hybridized DNA probes can be selectively illuminated for light detection.

22 Still another object of the present invention is to provide an optical switch
23 array assembly that is integrated in a substrate with an optical switch array and

1 at least a driving circuit so that all sites for DNA probe light synthesis can be
2 directly illuminated without any interference with the reactive liquid.

3 Still another object of the present invention is to provide an optical switch
4 array assembly that is integrated in a substrate with an optical switch array and
5 at least a driving circuit so that all hybridized DNA probes can be directly
6 illuminated without any cross talk between the adjacent hybridized probes.

7 Still another object of the present invention is to provide an optical switch
8 array assembly composed of an optical switch array and at least a driving
9 circuit can be batch produced simply using integrated circuit technology and
10 micromachining technology.

11 Still another object of the present invention is to provide an optical switch
12 array assembly for DNA probe light synthesis and hybridized DNA probe light
13 detection that is simple, cheap and timesaving.

14 Still another object of the present invention is to provide an optical switch
15 array assembly for DNA probe light synthesis and hybridized DNA probes light
16 detection that can be operated in any laboratory and any hospital.

17 In order to achieve the above object, in the present invention, an optical
18 switch array assembly is composed of a silicon substrate, an optical switch
19 array disposed in the substrate, a glass plate mounted on the top of the
20 substrate, and a DNA probe array disposed on the surface of the glass plate.
21 The substrate also contains a driving circuit for forcing each optical switch or
22 each group of optical switches on and off and perhaps an addressing circuit for
23 locating each optical switch or each group of optical switches. A plurality of
24

1 holes is also created in the substrate so that each hole is aligned with an
2 optical switch on the backside and guides an optical beam to the optical switch.

3 The optical switches are constructed from two parallel thin film mirrors
4 separated by an air gap. As well known, the transmission T of a loss less
5 Fabry-Perot cavity is a function of the reflectivities R_1 and R_2 of the mirrors and
6 of the air gap h between the mirrors:

7
$$T = [(1 - R_1)(1 - R_2)] / [(1 - \sqrt{R_1 R_2})^2 + 4\sqrt{R_1 R_2} \sin^2(2\pi h / \lambda)]$$

8 where λ is the working light wavelength.

9 This expression has maximum and minimum when the sine in the denominator
10 is respectively zero and one. Thus for h being a multiple of $\lambda/4$, the
11 transmission becomes

12
$$T_{\max} = [(1 - R_1)(1 - R_2)] / (1 - \sqrt{R_1 R_2})^2 \text{ for } h = 0, \lambda/2, \lambda, \dots$$

13
$$T_{\min} = [(1 - R_1)(1 - R_2)] / (1 + \sqrt{R_1 R_2})^2 \text{ for } h = \lambda/4, 3\lambda/4, \dots$$

14 The reflectivity of the Fabry-Perot cavity can be become zero only if the mirrors
15 are of equal reflectivity. In this case the above equation is always equal to one.
16 Thus, to get a maximal contract and a maximum reflectivity of the Fabry-Perot
17 cavity, the reflectivities R_1 and R_2 of the thin film mirrors must be as equal as
18 possible and as high as possible.

19 It is also known that the reflectivities R_1 and R_2 of the thin film mirrors
20 are maximum if their thickness is an odd multiple of $\lambda/(4n)$, where n is the
21 refractive index of the mirrors material. In this case R_1 and R_2 of the thin film
22 mirrors can be expressed by

23
$$R_{1,2} = \left(\frac{n^2 - n_0}{n^2 + n_0} \right)^2$$

1 where n_0 is the refractive index of the underlying medium.
2 In the present invention the thin film mirrors are made of amorphous silicon
3 carbide or plasma enhanced chemical vapor deposition (PECVD) silicon nitride
4 or lower pressure vapor chemical deposition (LPCVD) silicon nitride. For
5 ultraviolet light silicon dioxide, silicon nitride and silicon carbide are transparent,
6 thus there is no light loss due to absorption. It has been reported that the
7 refractive index of amorphous silicon carbide is 2.48 to 2.65, and the refractive
8 index of silicon nitride is 2.0 to 2.1.

9 For a freestanding thin film membrane the underlying medium is air and
10 $n_0=1$. Thus the above equation yields values of 52%-56% for $R_{1,2}$ of amorphous
11 silicon carbide and 40-36% for $R_{1,2}$ of silicon nitride.

12 The optical switches based on the Fabry-Perot cavity are operated by
13 electrostatic force. The top mirror of the Fabry-Perot cavity is supported by
14 several flexible beams. Without a voltage applied to the cavity, h equals to odd
15 multiple of $\lambda/4$ so that the transmission of the cavity reaches minimum or the
16 switch is closed. With a voltage applied to the cavity, the flexible beams bend
17 down and the supported mirror moves towards the bottom mirror so that h
18 equals to even multiple of $\lambda/4$. As a result, the transmission of the cavity
19 reaches maximum or the optical switch is opened. As shining light is
20 perpendicularly projected on the backside of the substrate, each hole guides a
21 light beam to a corresponding optical switch. When the optical switch is opened
22 or closed the light beam proceeds forward along the extension direction of the
23 hole or is reflected along a reverse direction.

24

1 The applied voltage is directed to a set of selective optical switches by
2 the combination of a driving circuit and an addressing circuit. The driving circuit
3 may be an electrical switch array. The addressing circuit may be a shift register
4 circuit. Both the driving circuit and the addressing circuit may be partially or fully
5 integrated in a same silicon substrate with the optical switch array.

6 Synthesizing DNA array with the optical switch array assembly using the
7 DMT process may take place as follows. First, a computer file is generated and
8 specifies, for each light illumination step, which optical switches in the optical
9 switch array assembly need to be on and which need to be off to generate a
10 particular predetermined light illumination pattern. Second, the glass plate of
11 the optical switch array assembly is coated with photoresist on the synthesis
12 surface and the optical switch array assembly is mounted in a holder or flow
13 cell so that the synthesis surface is applied with DMT-protected nucleotides
14 containing the desired base (adenine (A), cytosine (C), guanine (G), or thymine
15 (T)). The photoresist may be either positive or negative thus allowing
16 deprotection at locations exposed to the light or deprotection at locations not
17 exposed to the light, respectively. Thirdly, the optical switch array is
18 programmed for the appropriate configuration according to the desired
19 predetermined light illumination pattern, a shutter in an arc lamp is opened, the
20 synthesis surface is illuminated for the desired amount of time, and the shutter
21 is closed. Fourth, the photoresist is developed and etched. Exposure of the
22 glass plate to acid then cleaves the DMT protecting groups from regions of the
23 glass plate where the photoresist has been removed. The remaining
24 photoresist is then stripped. Fifth, DMT-protected nucleotides are coupled to

1 the deprotected oligonucleotides. Sixth, the glass plate of the optical switch
2 array assembly is re-coated with photoresist. These steps are repeated until
3 the DNA array synthesis is complete.

4 In the analysis procedure, at first, all the components in the sample are
5 labeled with tags such as fluorophores or enzymes. They are placed on the
6 glass plate of the optical switch array assembly for hybridization. If the sample
7 has a component being hybridized with probes on the glass plate, the
8 component is held on the corresponding regions.

9 The positions of the region emitting fluorescence can be determined by
10 selectively opening the corresponding optical switches of the optical switch
11 array assembly. From the positional information of the fluorescence emitting
12 regions, the probe species being hybridized with the sample components can
13 be determined.

14

15 BRIEF DESCRIPTION OF THE INVENTION

16 Figure 1A is a schematic diagram showing a DNA probe of an optical
17 switch array assembly of the present invention being synthesized by projecting
18 a light beam on a predetermined site by switching an optical switch on.

19 Figure 1B is a schematic diagram showing a hybridized DNA probe of an
20 optical switch array assembly of the present invention being detected by
21 projecting a light beam on the hybridized DNA probe by switching an optical
22 switch on.

23

24

1 Figure 2 is a perspective view of a cell cut from an optical switch array
2 assembly of the present invention, which comprises an optical switch and a
3 DNA probe.

4 Figure 3 is a cross-sectional view of an optical switch of the present
5 invention at a first fabrication step in which a first silicon dioxide layer, a first
6 amorphous silicon carbide layer, a second silicon dioxide layer, and a second
7 amorphous silicon carbide layer have been deposited on the surface of a
8 silicon substrate.

9 Figure 4 is a cross-sectional view of an optical switch of the present
10 invention at a second fabrication step in which an electrical interconnection, two
11 spacers, and a metal reflective layer have been formed on the two sides of the
12 silicon substrate, respectively.

13 Figure 5 is a cross-sectional view of an optical switch of the present
14 invention at a third fabrication step in which a refilled trench has been created
15 in the silicon substrate.

16 Figure 6 is a cross-sectional view of an optical switch of the present
17 invention at a fourth fabrication step in which a plane configuration of a Fabry-
18 Perot cavity has been defined in the silicon substrate.

19 Figure 7 is a cross-sectional view of an optical switch of the present
20 invention at a fifth fabrication step in which a light beam guiding hole has been
21 created on the backside of the silicon substrate.

22 Figure 8 is a cross-sectional view of an optical switch of the present
23 invention at a seventh fabrication step in which an air gap has been formed in

24

1 the silicon substrate and therefore a Fabry-Perot cavity based optical switch
2 has been constructed.

3 Figure 9 is a cross-sectional view of an optical switch of the present
4 invention at an eighth fabrication step in which a glass plate has been mounted
5 on the top of the silicon substrate.

6 Figure 10 is a cross-sectional view of an optical switch of the present
7 invention at a ninth fabrication step in which a DNA probe has been
8 synthesized on the surface of the glass plate.

9

10 DETAILED DESCRIPTION OF THE INVENTION

11 An optical switch array assembly of the present invention is shown in
12 FIG. 1A and FIG. 1B. As shown in FIG. 1A, the optical switch array assembly
13 consists of a silicon substrate 101, a plurality of closed optical switches 102
14 and a plurality of opened optical switches including an opened optical switches
15 103 that are disposed in the silicon substrate 101, a glass plate 104 mounted
16 on the top of the silicon substrate 101 with the periphery sealed, and a plurality
17 of DNA probes including DNA probe 112 disposed on the surface of the glass
18 plate 104. The optical switch array assembly also comprises a plurality of
19 electrical switches 107 and a plurality of logic gates 108 addressed by a vertical
20 shift register circuit 109 and a horizontal shift register circuit 110 for applying a
21 voltage 111 to the optical switches. The electrical switches, the logic gates, and
22 perhaps the register circuits are disposed in the silicon substrate 101. The
23 backside of the silicon substrate 101 is illuminated by a light source. A plurality
24 of the optical switches including optical switch 102 is off and the light beams

1 indicated by number 106 are reflected back. A plurality of the optical switches
2 including optical switch 103 is on and the light beam indicated by number 105
3 proceeds forward and reaches the front surface of the glass plate 104. This
4 induces the DNA probes including DNA probe indicated by number 112 being
5 synthesized on the light illuminated sites of the glass plate 104.

6 As shown in FIG.1B, the optical switch array assembly has the same
7 indication members as the optical switch array assembly shown in FIG. 1A,
8 except that the DNA probe 112 is hybridized with a fluorophore tag attached
9 sample DNA and therefore become a hybridized DNA probe 113. Under
10 illumination the hybridized DNA probe 113 can emit fluorescence 114. Then the
11 fluorescence can be detected by a photodetector. It is supposed that the kind of
12 the DNA probe 112 is known. So when the hybridized DNA probe 113 is
13 illuminated by selectively opening a corresponding optical switch, the kind of
14 the sample DNA can be determined by addressing the location of the opened
15 optical switch.

16 A single cell of a DNA probe array assembly of the present invention is
17 shown in FIG.2. The cell comprises a silicon substrate 201, a first amorphous
18 silicon carbide layer 202, a silicon dioxide layer 203, and a second amorphous
19 silicon carbide layer 204 which are sequentially laminated on the surface of the
20 silicon substrate 301, and a Fabry-Perot cavity disposed in the silicon substrate
21 201. The Fabry-Perot cavity is constructed of a first amorphous silicon carbide
22 membrane 205, an air gap 206, and a second amorphous silicon carbide
23 membrane 207. The second amorphous silicon carbide membrane 207 is
24 supported by four amorphous silicon carbide beams 208. Each amorphous

silicon carbide beam 208 is connected to the silicon substrate 201 through two anchors 209. A metal electrode 210 covers the amorphous silicon carbide beams 208 and the outer portion of the second amorphous silicon carbide membrane 207. The first amorphous silicon membrane 205 is coated with an anti-reflective layer 211 on the backside and aligned with an opened back hole 213. The area occupied by the Fabry-Perot cavity, is surrounded by etch stop walls 214. Each anchor 209 is connected to an etch stop wall 214. A reflective layer 215 is coated on the backside of the silicon substrate 301. A glass plate 217 is mounted on the top of the silicon substrate through two spacers 216. A DNA probe 218 is positioned on the front surface of the glass plate 217 and aligned with the bottom hole 213.

The thicknesses of the first amorphous silicon carbide membrane 205 and the second amorphous silicon carbide membrane 207 are designed to be equal to odd multiple of $\lambda / (4n_{SiC})$ so as to set their reflectivities to take maximum value, respectively. The original or static thickness of the air gap 206 is designed to be equal to odd multiple of $\lambda / 4$ so as to set the Fabry-Perot cavity to be in a minimum transmission state or a full reflective state.

Since the resistivity of the amorphous silicon carbide ranges from 10^{10} to 10^{14} ohm-cm, the electrode 210 can combine with the silicon substrate 201 to form a parallel plate capacitor. When a voltage apply to the capacitor, a resulted electrostatic force pulls the second amorphous silicon carbide membrane 207 down to the first amorphous silicon carbide membrane 205 and therefore reduce the thickness of the air gap 206. As soon as the thickness of

1 the air gap equals to even multiple of $\lambda / 4$, the Fabry-Perot cavity is driven into
2 a maximum transmission state.

3 The DNA probe 218 is generated by light illumination. The shape and
4 the size of the DNA probe 218 are duplicated from the hole 213 because the
5 light beam is introduced through the hole 213.

6 As an alternative, the mirrors of the Fabry-Perot cavity are made of
7 PECVD silicon nitride instead of amorphous silicon carbide. In this case the
8 stack structure of amorphous silicon carbide layer - silicon dioxide layer -
9 amorphous silicon layer is replaced by a stack structure of PECVD silicon
10 nitride layer - aluminum - PECVD silicon nitride layer.

11 As another alternative, the mirrors of the Fabry-Perot cavity are made of
12 LPCVD silicon nitride instead of amorphous silicon carbide. In this case the
13 stack structure of amorphous silicon carbide layer - silicon dioxide layer -
14 amorphous silicon layer is replaced by a stack structure of LPCVD silicon
15 nitride layer - silicon dioxide - LPCVD silicon nitride layer.

16 A first preferred fabrication process of the DNA probe array assembly of
17 the present invention comprises eight steps as shown in FIG. 3 to FIG. 10.
18 The first step as shown in FIG. 3, is to prepare a silicon substrate 301 and
19 deposit a stack structure of a first silicon dioxide layer 302, a first amorphous
20 silicon carbide layer 303, a second silicon dioxide layer 304, and a second
21 amorphous silicon carbide layer 305. The silicon substrate 301 may have been
22 undergone a standard MOSFET processing process. The MOS process is to
23 fabricate a plurality of MOS circuits including MOSFET switches arrays, MOS
24 gate circuits, and perhaps shift registers circuits (not shown in the figure). The

1 first silicon dioxide layer 302 is deposited by PECVD and has a thickness little
2 larger than $\lambda/(4n_{SiO_2})$ so that after completing a subsequent hole formation
3 process the thickness is just $\lambda/(4n_{SiO_2})$. Deposition parameters used are power
4 of 250 W, temperature of 300 centigrade, N₂O flow of 400sccm, SiH₄ flow of
5 40sccm and pressure of 240 mtorr, resulting in a deposition rate of 600
6 angstrom/min. The first amorphous silicon carbide layer 303 is deposited by
7 PECVD and has a thickness of odd multiple of $\lambda/(4n_{SiC})$. The deposition
8 system used is a dual frequency reactor. Deposition parameters used are 600
9 mTorr, 10 sccm SiH₄, 250 sccm CH₄, 300 sccm Ar, 300 centigrade, and a
10 power level of 60 W. A resulted amorphous silicon carbide layer 303 has low
11 stress of <50 MPa and high etch resistance. The second silicon dioxide layer
12 304 is deposited by a same PECVD process as above described and has a
13 thickness of odd multiple of $\lambda/4$. The second amorphous silicon carbide layer
14 305 is deposited by a same PECVD as above described and has a thickness of
15 odd multiple of $\lambda/(4n_{SiC})$. It is preferred that the thickness of the first
16 amorphous silicon carbide layer 303 is much larger than thickness of the
17 second amorphous silicon carbide layer 305 so that the first amorphous silicon
18 carbide layer 303 has a higher mechanical strength.
19

20 The second step as shown in FIG. 4, is to create an electrical
21 interconnection 306, a back reflective layer 307, and two spacers 308. To do
22 this, a 1500 angstrom thick aluminum is deposited on the both sides of the
23 silicon substrate 301 by electron beam evaporation. Patterning the aluminum
24 layer is carried out using an aluminum etch solution of

1 H₃PO₄:HAC:CH₃COOH=8:0.5:0.1:1. Then another patterned photoresist layer
2 is formed on the front surface of the silicon substrate 301. Using the patterned
3 aluminum layer as a base, a 3 micron thick aluminum layer is deposited.
4 Removing the photoresist layer under the aluminum layer results in two
5 aluminum tops mounted on the spacer 308. In this way, the thickness of the
6 spacers 308 is increased to be larger than 3 microns, but the thickness of the
7 electrical interconnection 307 is still kept unchanged.

8 The third step as shown in FIG. 5, is to form a refilled etch stop trench
9 309 and anchors (not shown in the figure). Firstly, a trench is created in the
10 second amorphous silicon carbide layer 305 and the second silicon dioxide
11 layer 304. Patterning of the second amorphous silicon carbide 305 is done by
12 dry etching in a reactive ion etcher (RIE) with a power of 60 W and a pressure
13 of 0.05 mbar. The gas flows are 70 sccm CF₄, 10 sccm SF₆ and 10 sccm O₂,
14 respectively. In these conditions an etch rate of 800 angstrom/ min is obtained.
15 The etch selectivities to oxide and photoresist are 1.2 and 0.7, respectively.
16 The second silicon dioxide layer 304 is then etched in a diluted HF solution.
17 Finally, the trench is refilled with a sandwiched plug. To this end, a lining layer
18 of amorphous silicon is deposited by PECVD using a patterned photoresist
19 layer as a mask. Then a central layer of silicon nitride is deposited by PECVD.
20 The composite layer of the amorphous silicon and the amorphous nitride which
21 is disposed in the outer portion of the refilled trench, is lifted off by resolving the
22 underlying photoresist layer.

23 The fourth step as shown in FIG. 6, is to define the plane layout of a
24 Fabry-Perot cavity. The second amorphous silicon carbide layer 305 is

1 patterned again. Etching of the amorphous silicon carbide layer 305 is
2 performed by a same process as above described. After etching a window 310
3 is created and the refilled trench 309 now becomes an etch stop wall 309.

4 The fifth step as shown in FIG. 7, is to create a hole 311 on the backside
5 of the silicon substrate 301. To do this, a 9-10 micron thick positive photoresist
6 is applied and patterned to form an etch open. The aluminum layer in the open
7 is etched by a same process as above described. Then the revealed silicon in
8 the open is etched in a Bosch etcher. The etch proceeds almost through the
9 silicon substrate 301 and finely stops on the first silicon dioxide layer 302. The
10 diameter of the hole 311 on the bottom ranges from 4 to 30 microns, typically
11 10 microns. The revealed portion of the first silicon dioxide layer 302 is little
12 etched and therefore can be used as an anti-reflective film 312.

13 The sixth step as shown in FIG. 8, is to etch the second silicon dioxide
14 layer 304 surrounding by the etch stop walls including etch stop wall 309. As a
15 result, a first amorphous silicon carbide membrane 313, four amorphous silicon
16 carbide beams 314, an air gap 315, and a second amorphous silicon carbide
17 membrane 316 are constructed. The two opposite side edges of each
18 amorphous silicon carbide beam 314 are supported by two anchors formed in
19 the process for creating the refilled trenches 309. The size of the first
20 amorphous silicon carbide membrane 313 ranges from 4×4 to 50×50 microns,
21 typically 20×20 microns. The length and width of the amorphous silicon carbide
22 beams 314 range from 4 to 30 microns, typically 10 microns, and from 1 to 5
23 microns, typically 2 microns, respectively.

24

1 The seventh step as shown in FIG. 9, is to mount a glass plate 317 on
2 the top of the silicon substrate 301 through the spacers 308. The backside of
3 the glass plate 316 may be coated with an anti-reflective layer. The periphery of
4 the glass plate 316 may be sealed to protect the optical switches during a
5 subsequent DNA synthesis process. The glass plate 317 is disposable, but the
6 optical switch array may be used repeatedly. So it is preferable that the sealing
7 means is detachable.

8 The eighth step as shown in FIG. 10, is to synthesize DNA probes
9 including DNA probe 318 on the surface of the glass plate 317. The DNA
10 probes including DNA probe 318 are synthesized using photolabile protecting
11 groups and direct photodeprotection. In this approach, the surface of the glass
12 plate 317 is modified with photolabile protecting groups. A first group of sites for
13 DNA probe synthesis are illuminated through a first group of the opened optical
14 switches, yielding reactive hydroxyl groups thereon. A 3' activated
15 deoxynucleoside, protected at the 5' hydroxyl with a photolabile group, is then
16 provided to the surface such that coupling occurs at the sites. Following
17 capping, and oxidation, the glass plate 317 is rinsed and a second group of
18 sites are illuminated through a second group of the optical switches to expose
19 additional hydroxyl groups for coupling. A second 5' protected activated
20 deoxynucleoside base is presented to the surface. The selective
21 photodeprotection and coupling cycles are repeated until a desired set of
22 probes is obtained.

23

24

1 As an alternative, the DNA probes including DNA probe 318 are
2 synthesized using acid-labile 4,4'-dimethoxytrityl (DMT) protecting groups and a
3 photoresist.

4 As another alternative, the DNA probes including DNA probe 318 are
5 synthesized using DMT protecting groups and a polymer film that contains a
6 photoacid generator (PAG).

7 A second preferred fabrication process of the DNA probe array
8 assembly of the present invention utilizes a stack structure of a first aluminum
9 layer, a first PECVD silicon nitride layer, a second aluminum layer, and a
10 second PECVD silicon nitride layer instead of the stack structure of a first
11 silicon dioxide layer, a first amorphous silicon carbide layer, a second silicon
12 dioxide layer, and a second amorphous silicon carbide layer. The difference
13 between the two processes includes the following items:

14 1. Deposition parameters used for the PECVD nitride layer are SiH₄ 170
15 sccm, NH₃ 30 sccm, temperature 300 centigrade, pressure 450 mtorr, power
16 250 w, resulting in a deposition rate of 160 angstrom/min.

17 2. RIE etching parameters used for the PECVD nitride layer are CHF₄ 7.5
18 sccm, N₂ 42.5 sccm, power 60W, and pressure 37.5 mtorr, resulting in an etch
19 rate of 250 angstrom/min.

20 3. The refilled trenches or etch stop walls are refilled with silicon dioxide instead
21 of both amorphous silicon and silicon dioxide.

22 4. The first aluminum layer revealed by creating a hole needs to be removed by
23 etching, and then a $\lambda/(4n_{SiO_2})$ thick silicon dioxide layer is deposited thereon to
24 form an anti-reflective film.

1
2 A third preferred fabrication process of the DNA probe array assembly of
3 the present invention utilizes a stack structure of a first silicon dioxide layer, a
4 first LPCVD silicon nitride layer, a second silicon dioxide layer, and a second
5 LPCVD silicon nitride layer instead of the stack structure of a first silicon
6 dioxide layer, a first amorphous silicon carbide layer, a second silicon dioxide
7 layer, and a second amorphous silicon carbide layer. The difference between
8 the two processes includes the following items:

9 1. The MOSFET circuits disposed in the silicon substrate have not undergone a
10 metallization step at the beginning of the fabrication process.

11 2. Deposition parameters used for the LPCVD nitride layer are SiH₂Cl₂ 170
12 sccm, NH₃ 30 sccm, temperature 850 centigrade, pressure 150 mtorr, power
13 400 w, resulting in a deposition rate of 90 angstrom/min.

14 3. RIE etching parameters used for the LPCVD nitride layer are CHF₄ 7.5 sccm,
15 N₂ 42.5 sccm, power 60W, and pressure 37.5 mtorr, resulting in an etch rate of
16 250 angstrom/min.

17 4. Metallization for creating electrical interconnections (including
18 interconnections for prefabricated circuits) and spacers is carried out as a final
19 fabrication step.

20
21 The above description is illustrative and not restrictive. Many variations
22 of the invention will become apparent to those of skill the art upon review of this
23 disclosure. Merely by way of preferred embodiment, while the invention is
24 illustrated primarily with regard to a Fabry-Perot cavity based optical switch

1 array assembly, the invention is not so limited. The scope of the invention
2 should, therefore, be determined not with reference to the above description,
3 but instead should be determined with reference to the appended claims along
4 with their full scope of equivalents.

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